

Abstract

An object of the present invention is to provide a pulse width modulation amplifier which is capable of suppressing an output distortion due to a power voltage regulation, without deteriorating efficiency. The pulse width modulation amplifier relating to the present invention includes,

a first series circuit including a first switching element 41 connected to a positive pole side of a first direct-current power source V_{cc1} , and a second switching element 42, one end of the load circuit being connected to a connecting point between the first switching element 41 and the second switching element 42,

a second series circuit including a third switching element 51 connected to a positive pole side of a second direct-current power source V_{cc2} , and a fourth switching element 52, the other end of the load circuit being connected to a connecting point between the third switching element 51 and the fourth switching element 52, and

a driving circuit which turns on a set of the first switching element 41 and the fourth switching element 52, and a set of the second switching element 42 and the third switching element 51 set-by-set, so that each

of the sets is alternately put into on-state.